

X-1490 US
10/812,550

PATENT
Conf. No.: 3439



IN THE UNITED STATES PATENT OFFICE

Applicants: Rajat Aggarwal et al.
Assignee: Xilinx, Inc.
Title: Method, System, and Apparatus for Incremental Design in
Programmable Logic Devices Using Floorplanning
Serial No.: 10/812,550 File Date: 03-29-04
Examiner: Paul Dinh Art Unit: 2825
Docket No.: X-1490 US Conf. No.: 3439

Mail Stop AMENDMENT
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT IN RESPONSE TO THE FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action mailed from the Patent Office on May 22, 2006, please replace/substitute the following claims as indicated.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.